

APL502J

500V 52A 0.090Ω

LINEAR MOSFET

Linear Mosfets are optimized for applications operating in the Linear region where concurrent high voltage and high current can occur at near DC conditions (>100 msec).



Popular SOT-227 Package

Higher Power Dissipation

SOA Rated

RoHS Compliant



MAXIMUM RATINGS

All Ratings: $T_C = 25^{\circ}C$ unless otherwise specified.

Symbol	Parameter	APL502J	UNIT	
V _{DSS}	Drain-Source Voltage	500	Volts	
I _D	Continuous Drain Current @ T _C = 25°C	52	Amps	
I _{DM}	Pulsed Drain Current ①	208		
V_{GS}	Gate-Source Voltage Continuous	±30	Volts	
V _{GSM}	Gate-Source Voltage Transient	±40	Voits	
P _D	Total Power Dissipation @ T _C = 25°C	568	Watts	
	Linear Derating Factor	4.55	W/°C	
T_J , T_{STG}	Operating and Storage Junction Temperature Range	-55 to 150	°C	
T _L	Lead Temperature: 0.063" from Case for 10 Sec.	300] ~	
I _{AR}	Avalanche Current (1) (Repetitive and Non-Repetitive)	52	Amps	
E _{AR}	Repetitive Avalanche Energy ①	50	mJ	
E _{AS}	Single Pulse Avalanche Energy ⁴	3000	1113	

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions / Part Number	MIN	TYP	MAX	UNIT
BV _{DSS}	Drain-Source Breakdown Voltage (V_{GS} = 0V, I_D = 250 μ A)	500			Volts
I _{D(ON)}	On State Drain Current $^{\textcircled{2}}(V_{DS} > I_{D}(ON) \times R_{DS}(ON) \text{ Max, } V_{GS} = 15V)$	52			Amps
R _{DS(ON)}	Drain-Source On-State Resistance ^② (V _{GS} = 15V, 26A)			0.09	Ohms
I _{DSS}	Zero Gate Voltage Drain Current ($V_{DS} = 500V, V_{GS} = 0V$)			25	μΑ
	Zero Gate Voltage Drain Current (V _{DS} = 400V, V _{GS} = 0V, T _C = 125°C)			250	
I _{GSS}	Gate-Source Leakage Current (V _{GS} = ±30V, V _{DS} = 0V)			±100	nA
V _{GS(TH)}	Gate Threshold Voltage $(V_{DS} = V_{GS}, I_{D} = 2.5 \text{mA})$	2		4	Volts

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

DYNAMIC CHARACTERISTICS

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C _{iss}	Input Capacitance	V _{GS} = 0V		7600	9000	
C _{oss}	Output Capacitance	V _{DS} = 25V		1280	1810	pF
C _{rss}	Reverse Transfer Capacitance	f = 1 MHz		620	930	
t _{d(on)}	Turn-on Delay Time	V _{GS} = 15V		13	26	
t _r	Rise Time	$V_{DD} = 0.5 V_{DSS}$		24	48	ns
t _{d(off)}	Turn-off Delay Time	I _D = 52A @ 25°C		58	87	
t _f	Fall Time	$R_G = 0.6\Omega$		14	17	

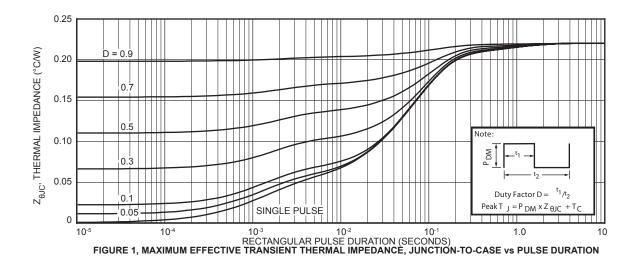
THERMAL CHARACTERISTICS

Symbol	Characteristic	MIN	TYP	MAX	UNIT
R _{øJC}	Junction to Case			.22	°C/W
V	RMS Voltage (50-60 Hz Sinusoidal Waveform From Terminals to Mounting Base for 1 Min.)	2500			Volts
W _T	Package Weight		1.03		OZ
			29.2		g
Torque	Maximum Torque for Device Mounting Screws and Electrical Terminations.			10	lb•in
				1.1	N•m
SOA1	Safe Operating Area $V_{DS} = 400 \text{ V}, I_{DS} = 0.75 \text{A}, t = 20 \text{ sec.}, T_{C} = 60 ^{\circ}\text{C}$	300			Watts

⁽¹⁾ Repetitive Rating: Pulse width limited by maximum junction temperature.

2 Pulse Test: Pulse width < 380 μ S, Duty Cycle < 2%

Microsemi reserves the right to change, without notice, the specifications and information contained herein.



 T_J (°C) Z_{EXT} are the external thermal impedances: Case to sink, 0.052 0.155 0.0126 Dissipated Power sink to ambient, etc. Set to (Watts) zero when modeling only 0.061 0.423 67.45 the case to junction.

 $[\]stackrel{\textcircled{3}}{=}$ See MIL-STD-750 Method 3471 $\stackrel{\textcircled{4}}{=}$ Starting T_i = +25°C, L = 2.22mH, R_G = 25 Ω , Peak I_L = 52A

0.7

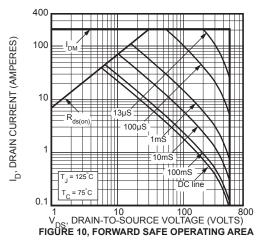
 $\rm T_{C},$ CASE TEMPERATURE (°C) FIGURE 9, THRESHOLD VOLTAGE vs TEMPERATURE

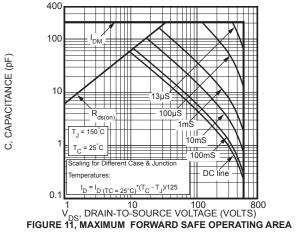
0.5

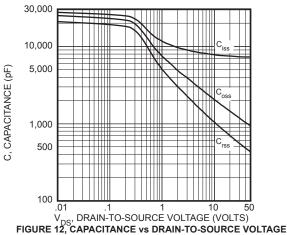
T,, JUNCTION TEMPERATURE (°C)

FIGURE 8, ON-RESISTANCE vs. TEMPERATURE

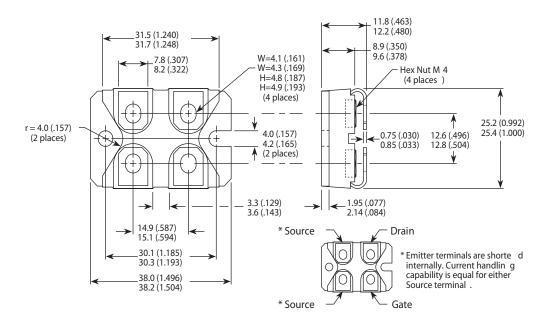








SOT-227 (ISOTOP®) Package Outline



Dimensions in Millimeters

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