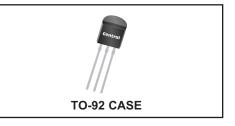
2N3819

SILICON N-CHANNEL JFET



www.centralsemi.com

The CENTRAL SEMICONDUCTOR 2N3819 is a silicon N-Channel JFET designed for RF amplifier and mixer applications.



MARKING: FULL PART NUMBER

MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Drain-Gate Voltage	V_{DG}	25	V
Drain-Source Voltage	V_{DS}	25	V
Gate-Source Voltage	V_{GS}	25	V
Continuous Gate Current	I_{G}	10	mA
Power Dissipation	P_{D}	360	mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C

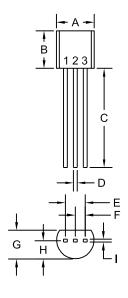
Operating and	Storage Junction Temperature	^I J, ^I stg	-65 to +150	-0
ELECTRICAL SYMBOL	CHARACTERISTICS: (T _A =25°C TEST CONDITIONS	unless otherwise no	ited)	UNITS
I _{GSS}	V _{GS} =15V		2.0	nA
I _{GSS}	V _{GS} =15V, T _A =100°C		2.0	μΑ
I _{DSS}	V _{DS} =15V	2.0	20	mA
BVGSS	I _G =1.0μA	25		V
V _{GS(OFF)}	V_{DS} =15V, I_{D} =2.0nA		8.0	V
V_{GS}	V_{DS} =15V, I_{D} =200 μ A	0.5	7.5	V
$ Y_{fs} $	V_{DS} =15V, V_{GS} =0, f=1.0MHz	2.0	6.5	mS
$ Y_{fs} $	V_{DS} =15V, V_{GS} =0, f=100MHz	1.6		mS
Y _{os}	V_{DS} =15V, V_{GS} =0, f=1.0kHz		50	μS
C _{iss}	V_{DS} =15V, V_{GS} =0, f=1.0MHz		8.0	pF
C _{rss}	V_{DS} =15V, V_{GS} =0, f=1.0MHz		4.0	pF

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TO-92 CASE - MECHANICAL OUTLINE



DIMENSIONS							
	INCHES		MILLIMETERS				
SYMBOL	MIN	MAX	MIN	MAX			
A (DIA)	0.175	0.205	4.45	5.21			
В	0.170	0.210	4.32	5.33			
С	0.500	-	12.70	-			
D	0.016	0.022	0.41	0.56			
Е	0.100		2.54				
F	0.050		1.27				
G	0.125	0.165	3.18	4.19			
Н	0.080	0.105	2.03	2.67			
	0.015		0.38				

TO-92 (REV: R1)

LEAD CODE:

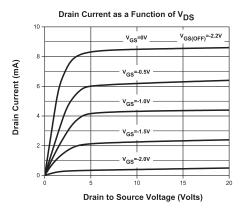
- 1) Drain
- 2) Gate
- 3) Source

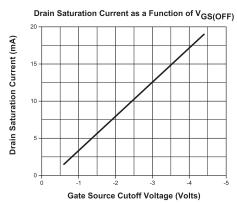
MARKING:

FULL PART NUMBER

TYPICAL ELECTRICAL CHARACTERISTICS

R1



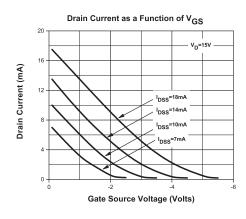


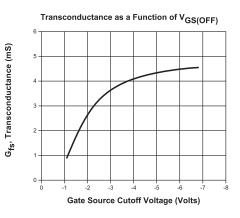
R1 (9-January 2014)

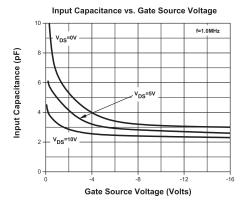
2N3819

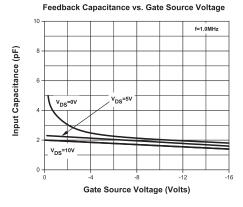
SILICON N-CHANNEL JFET

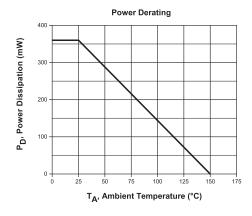












R1 (9-January 2014)

OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- · Inventory bonding
- · Consolidated shipping options

- · Custom bar coding for shipments
- · Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free guick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- · Custom electrical curves
- · Environmental regulation compliance
- · Customer specific screening
- · Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- · Application and design sample kits
- Custom product and package development

REQUESTING PRODUCT PLATING

- 1. If requesting Tin/Lead plated devices, add the suffix "TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
- 2. If requesting Lead (Pb) Free plated devices, add the suffix "PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

CONTACT US

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