MT9V034 Evaluation Board User's Manual

Evaluation Board Overview

The evaluation boards are designed to demonstrate the features of ON Semiconductor's image sensors products. This headboard is intended to plug directly into the Demo 2X system. Test points and jumpers on the board provide access to clock, I/Os and other miscellaneous signals.

Features

- Clock Input
 - Default 27 MHz crystal oscillator
 - Optional Demo 2X controlled MClk
- Two Wire Serial Interface
 - Selectable base address
- Parallel Interface
- Serial LVDS Interface
- ROHS Compliant



ON Semiconductor®

www.onsemi.com

EVAL BOARD USER'S MANUAL



Figure 1. MT9V034 Evaluation Board

Block Diagram

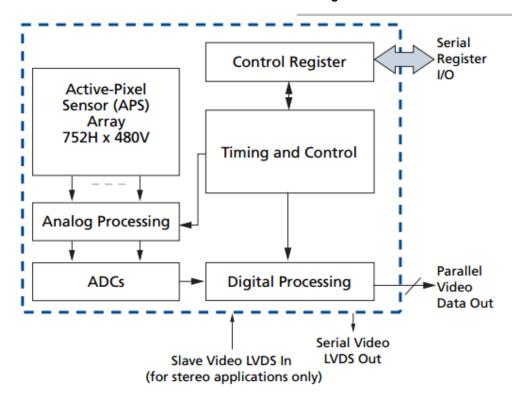


Figure 2. Block Diagram of MT9V034C12STCH-GEVB

Top View

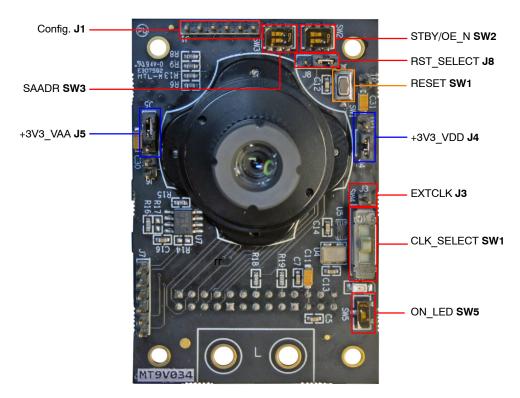


Figure 3. Top View of Evaluation Board – Default Jumpers

Bottom View

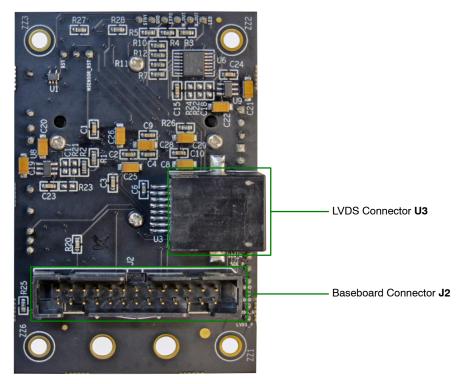


Figure 4. Bottom View of the Evaluation Board – Connector

Jumper Pin Locations

The jumpers on headboards start with Pin 1 on the leftmost side of the pin. Grouped jumpers increase in pin size with each jumper added.

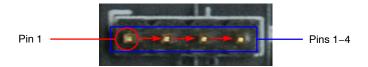


Figure 5. Pin Locations for a Single Jumper.

Pin 1 is Located at the Leftmost Side and Increases as it Moves to the Right



Figure 6. Address Switch Locations in their Default Positions.

The First Switch (ADR0) and the Second Switch (ADR1) of SW3 are Set to ON



Figure 7. Switch Descriptions of Switch SW4 in their Default Positions.

The First Switch (STDBY) is Set to OFF While the Second Switch (OE_N) is Set to ON

Jumper/Header Functions & Default Positions

Table 1. JUMPERS AND HEADERS

Jumper/Header No.	Jumper/Header Name	Pins	Description
J1	Config.	Open (Default)	Connects to various sensor's settings
J3	EXTCLK	Open (Default)	For connection to external clock
J4	+3V3_VDD	1-2 (Default)	Connects to on-board +3V3_VDD power supply
		Open	External power supply connection
J5	+3V3_VAA	1-2 (Default)	Connects to on-board +3V3_VAA power supply
		Open	External power supply connection
J8	RST_SELECT	2-3 (Default)	Reset set to SW1
SW1	RESET	N/A	When pushed, 400 ms reset signal will be sent to MT9V032
SW2	STDBY/OE_N	STDBY Off (Default)	EEPROM Address set to 0xA8
		STDBY On	EEPROM Address set to 0xAC
		OE_N On (Default)	EEPROM Address set to 0xA4
		OE_N Off	EEPROM Address set to 0xA0

Table 1. JUMPERS AND HEADERS (continued)

Jumper/Header No.	Jumper/Header Name	Pins	Description
SW3	SAADR	ADR1 On, ADR0 On (Default)	Address set to 0xB8
		ADR1 On, ADR0 Off	Address set to 0xB0
		ADR1 Off, ADR0 On	Address set to 0x98
		ADR1 Off, ADR0 Off	Address set to 0x90
SW4	CLK_SELECT	Position 1 (Default)	Connects to on-board 27 MHz oscillator
		Position 2	Connects to on-board 27 MHz oscillator
		Position 3	Connects to EXTCLK from J3
SW5	ON_LED	On (Default)	Connects LED indicator to +VDD_BUS
		Off	Turn off LED indicator

Interfacing to ON Semiconductor Demo 2X Baseboard

The ON Semiconductor Demo 2X baseboard has a similar 26-pin connector which mates with J2 of the

headboard. The four mounting holes secure the baseboard and the headboard with spacers and screws.

ON Semiconductor and in are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor designed, including compliance with may be provided in ON Semiconductor designed, intended, or authorized experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and e

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81-3-5817-1050 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative